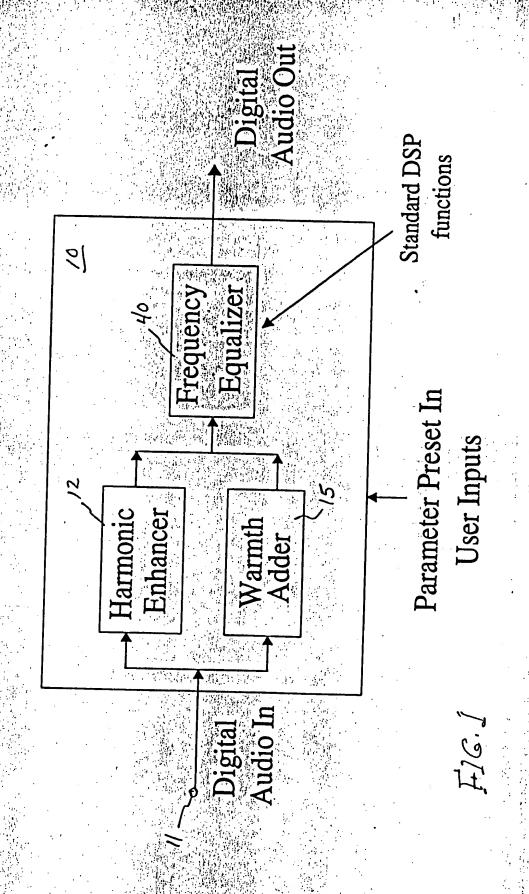
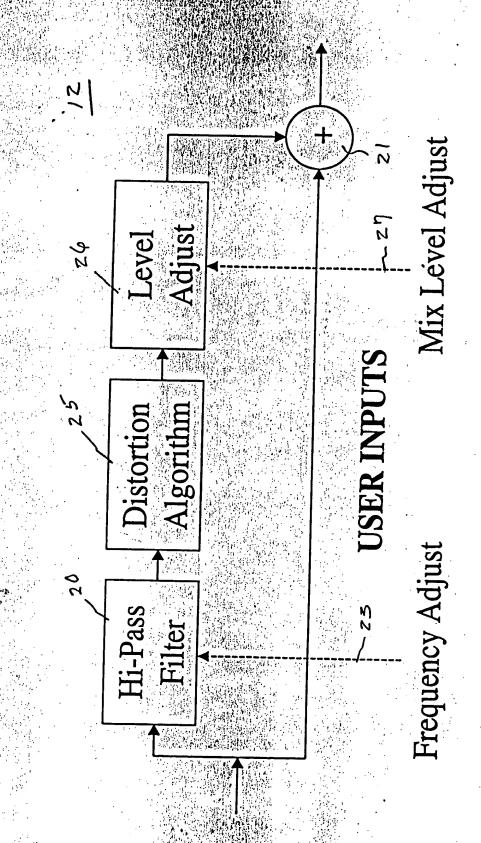
Processor Block Diagram



Harmonic Enhancer Diagram



F16.2

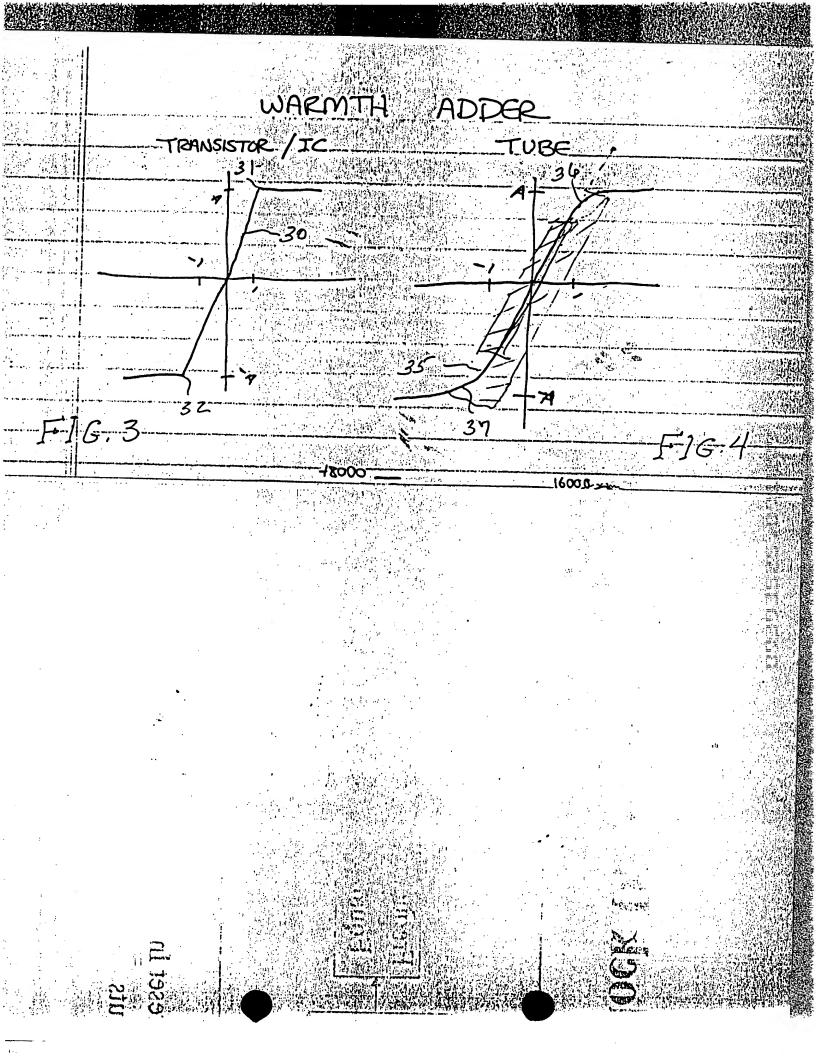


FIG.5 frequency FIG. 6